

Notice of References Cited	Application/Control No. 10/696,865		Applicant(s)/Patent Under Reexamination DOERING ET AL.	
	Examiner Dillon Cody		Art Unit 2183	Page 1 of 1

U.S. PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Name	Classification
*	A	US-6,721,884	04-2004	De Oliveira Kastrup Pereira et al.	713/2
*	B	US-6,023,755	02-2000	Casselman, Steven M.	712/37
	C	US-			
	D	US-			
	E	US-			
	F	US-			
	G	US-			
	H	US-			
	I	US-			
	J	US-			
	K	US-			
	L	US-			
	M	US-			

FOREIGN PATENT DOCUMENTS

*		Document Number Country Code-Number-Kind Code	Date MM-YYYY	Country	Name	Classification
	N					
	O					
	P					
	Q					
	R					
	S					
	T					

NON-PATENT DOCUMENTS

*		Include as applicable: Author, Title Date, Publisher, Edition or Volume, Pertinent Pages)
	U	Hastie, N; Cliff, R. "The Implementation of Hardware Subroutines on Field Programmable Gate Arrays." Proceedings of the IEEE. IEEE 1990 Custom Integrated Circuits Conference. 13-16 May 1990. Pages 31.4.1-4
	V	
	W	
	X	

*A copy of this reference is not being furnished with this Office action. (See MPEP § 707.05(a).)
Dates in MM-YYYY format are publication dates. Classifications may be US or foreign.